FIG 1

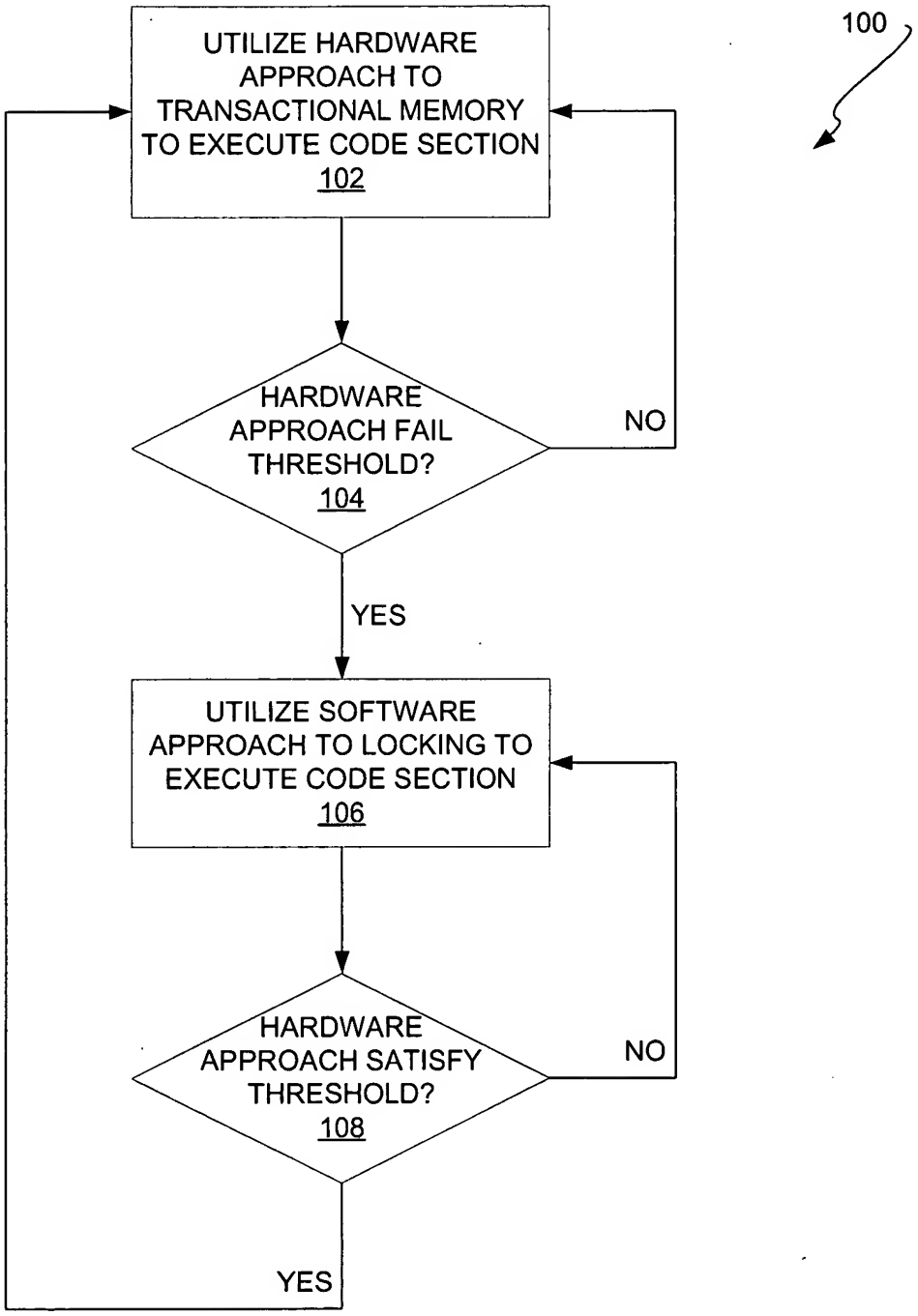

100
UTILIZE HARDWARE APPROACH TO TRANSACTIONAL MEMORY TO EXECUTE CODE SECTION
102
HARDWARE APPROACH FAIL THRESHOLD?
104
NO
YES
UTILIZE SOFTWARE APPROACH TO LOCKING TO EXECUTE CODE SECTION
106
HARDWARE APPROACH SATISFY THRESHOLD?
108
NO
YES

FIG 2

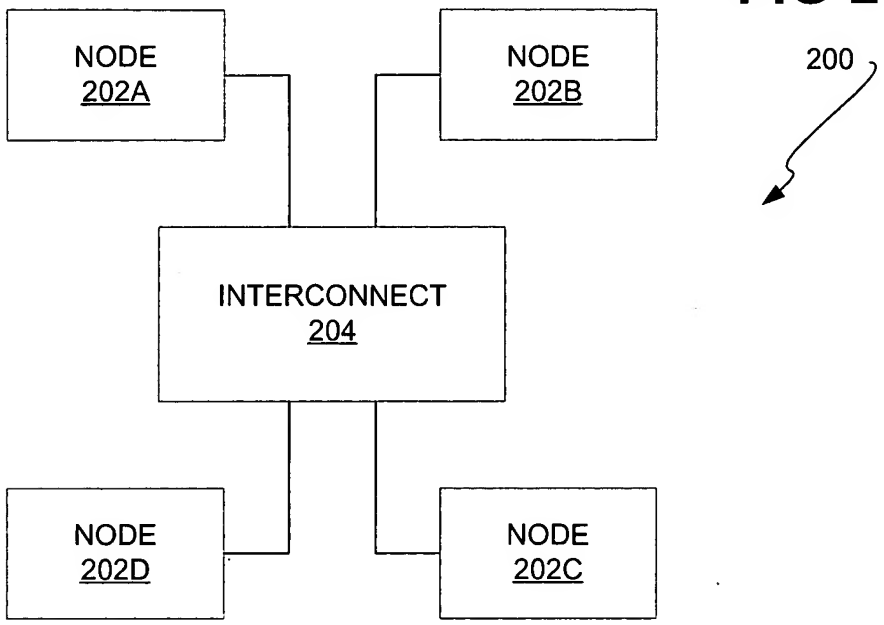

NODE
202A
NODE
202B
200
INTERCONNECT
204
NODE
202D
NODE
202C

FIG 3

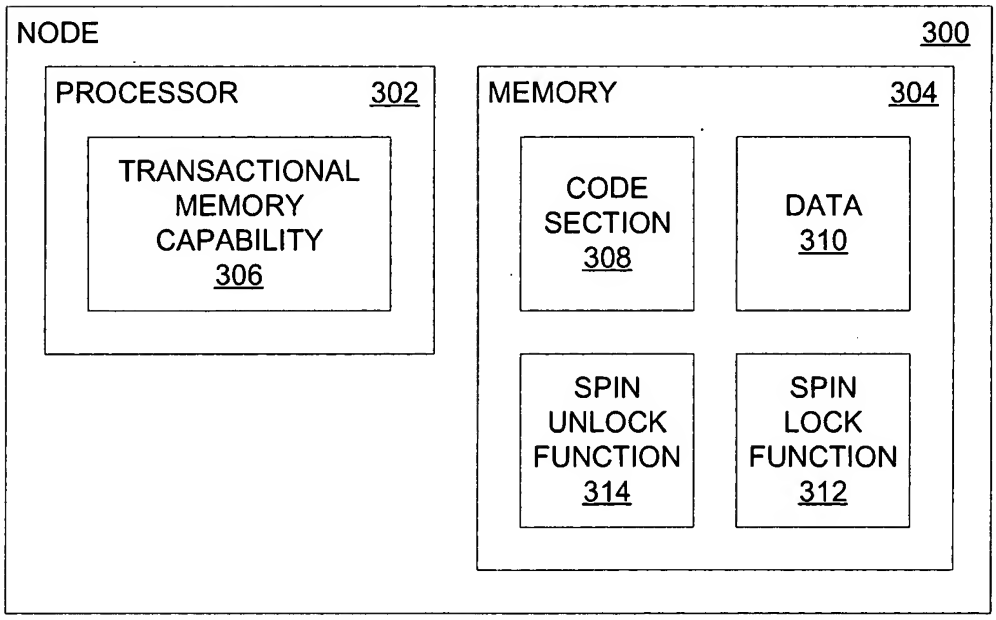

NODE
300
PROCESSOR
302
TRANSACTIONAL MEMORY CAPABILITY
306
MEMORY
304
CODE SECTION
308
DATA
310
SPIN UNLOCK FUNCTION
314
SPIN LOCK FUNCTION
312

FIG 4

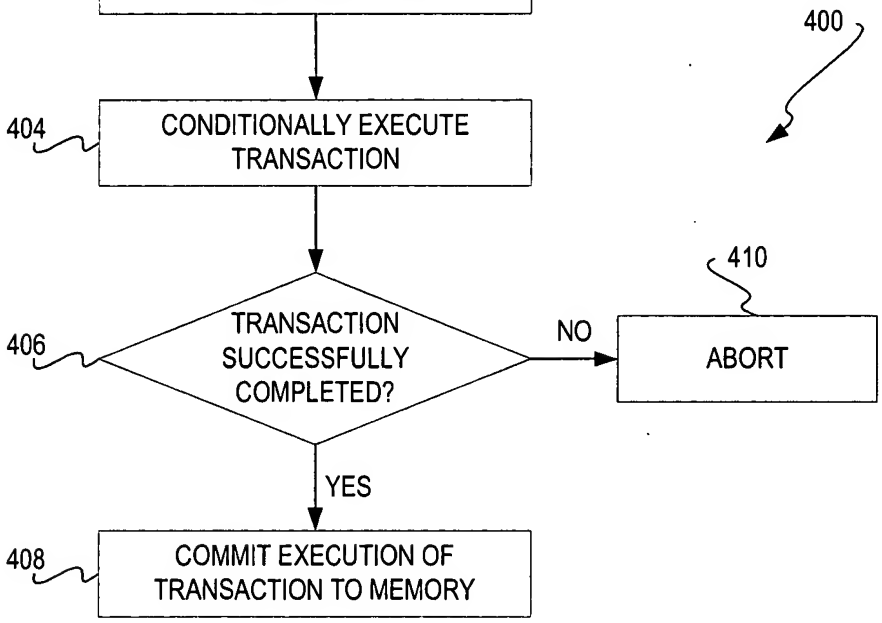
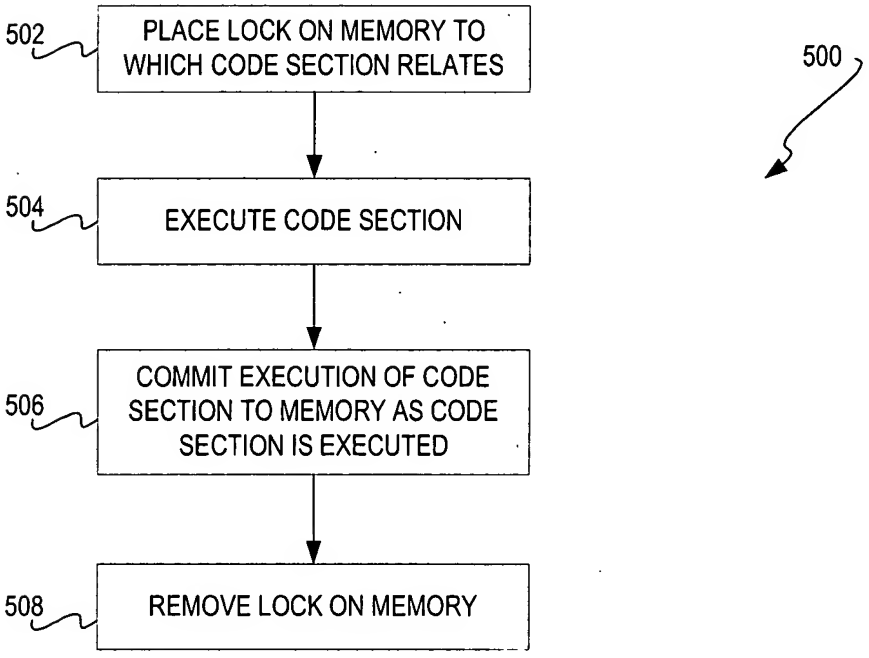

400
402
START TRANSACTION INCLUSIVE OF CODE SECTION
404
CONDITIONALLY EXECUTE TRANSACTION
406
TRANSACTION SUCCESSFULLY COMPLETED?
NO
410
ABORT
YES
408
COMMIT EXECUTION OF TRANSACTION TO MEMORY

FIG 5


500
502
PLACE LOCK ON MEMORY TO WHICH CODE SECTION RELATES
504
EXECUTE CODE SECTION
506
COMMIT EXECUTION OF CODE SECTION TO MEMORY AS CODE SECTION IS EXECUTED
508
REMOVE LOCK ON MEMORY